



Estd: 1998

**MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE**  
(UGC-AUTONOMOUS INSTITUTION)

Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi

NAAC Accredited with A+ Grade, NIRF India Rankings 2025 - Band: 201-300 & NBA Accredited - B.Tech. (CIVIL, CSE, ECE, EEE, MECH,CST), MBA & MCA  
Examination Section

Schedule - B.Tech IV Year I Semester (R23) Honors in Computer Science & Engineering and Electronics and Communication Engineering Mid Term Test -I  
Examinations September 2026

**Academic Year – 2026-27 (For 2023 Admitted batch)**

| DATE / DAY           | Timings                 | CSE                                     | ECE                                         |
|----------------------|-------------------------|-----------------------------------------|---------------------------------------------|
| 21.09.2026<br>Monday | 09:30 am to<br>11:00 am | Quantum Computing 23HDCSE104            | Testing of Digital VLSI Circuits 23HDECE103 |
|                      | 01:30 pm to<br>03:00 pm | Software Defined Data Center 23HDCSE105 | FPGA Architecture 23HDECE105                |

Note: (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.

(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.

  
CONTROLLER OF EXAMINATIONS

Copy to: (i) P.A to Principal (ii) CSE

Soft Copy - Library

SAO(GAD)

ECE

Transport Incharge

Placement Cell

Deputy Warden

  
PRINCIPAL

PRINCIPAL

Principal

Madanapalle Institute of  
VP-Administration Dean-Admin  
Technology & Science